



Attorney's Docket No. 4735.P005

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Patent Application of:

Soma et al.

Application No.: 10/665,970

Examiner: Bryan Bui

Filed: September 18, 2003

Art Unit: 2863

For: CHARACTERIZATION OF RADIO
FREQUENCY (RF) SIGNALS USING
WAVELET-BASED PARAMETER
EXTRACTION

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Commissioner for Patents
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INFORMATION DISCLOSURE STATEMENT

Sir:

Enclosed is a copy of Information Disclosure Citation Form PTO-1449 or PTO/SB/08 together with copies of the documents cited on that form. It is respectfully requested that the cited documents be considered and that the enclosed copy of Information Disclosure Citation Form PTO-1449 or PTO/SB/08 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s).

Pursuant to 37 C.F.R. § 1.97, the submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made and is not to be construed as an admission that the information cited in this statement is material to patentability.

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Yuko Tanaka

(Typed or printed name of person mailing correspondence)

Y. Tanaka

(Signature of person mailing correspondence)

Pursuant to 37 C.F.R. § 1.97, this Information Disclosure Statement is being submitted under one of the following (as indicated by an "X" to the left of the appropriate paragraph):

 X 37 C.F.R. §1.97(b).

 37 C.F.R. §1.97(c). If so, then enclosed with this Information Disclosure Statement is one of the following:

 A statement pursuant to 37 C.F.R. §1.97(e) or

 A check for \$180.00 for the fee under 37 C.F.R. § 1.17(p).

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- (1) A statement pursuant to 37 C.F.R. §1.97(e); and
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If there are any additional charges, please charge Deposit Account No. 02-2666.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Dated: 4/19/2006

Jan Little-Washington
Jan Little-Washington
Reg. No. 41,181

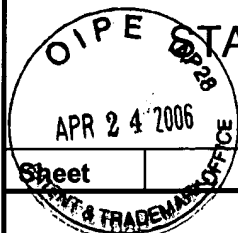
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Los Angeles, CA 90025
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Substitute for Form 1449/PTO

INFORMATION DISCLOSURE

STATEMENT BY APPLICANT

(use as many sheets as necessary)



Complete if Known

Application Number	10/665,970
Filing Date	September 18, 2003
First Named Inventor:	Soma et al.
Art Unit	2863
Examiner Name	Bryan Bui
Attorney Docket Number	4735.P005

Sheet 1 of 2

U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (If known)				
	1	US-	6,295,315 B1	09-25-2001	Frisch et al.	
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FOREIGN PATENT DOCUMENTS

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Substitute for Form 1449/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>				Complete if Known	
				Application Number	10/665,970
				Filing Date	September 18, 2003
				First Named Inventor:	Soma et al.
				Art Unit	2863
				Examiner Name	Bryan Bui
Sheet	2	of	2	Attorney Docket Number	4735.P005
NON PATENT LITERATURE DOCUMENTS					
Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published			T ²
	2	BOUWMAN, F. et al., "Application of Joint Time-Frequency Analysis in Mixed Signal Testing," Proc. IEEE International Test Conference, pp. 747-756, Washington, DC, 1994.			
	3	YAMAGUCHI, T.J. et al., "Dynamic Testing of ADCs Using Wavelet Transforms," Proc. IEEE International Test Conference, pp. 379-388, Washington, DC, 1997.			
	4	SUNTER, S. et al., "BIST for Phase-Locked Loops in Digital Applications," Proc. IEEE International Test Conference, pp. 532-540, Sept. 28-30, 1999, Atlantic City, NJ.			
	5	MASON, R. et al., "Mixed Signal DFT at GHz Frequencies," J. of Electronic Testing: Theory and Applications 15, pp. 31-39, 1999.			
	6	ROH, J. et al., "Subband Filtering Scheme for Analog and Mixed-Signal Circuit Testing," Proc. IEEE International Test Conference, pp. 221-229, Atlantic City, NJ, 1999.			
	7	YAMAGUCHI, T.J. et al., "Jitter Measurements of a PowerPC Microprocessor Using An Analytic Signal Method," Proc. IEEE International Test Conference, pp. 955-964, Atlantic City, NJ, 2000.			
	8	CHAN, A.H. et al., "A Synthesizable, Fast and High-Resolution Timing Measurement Device Using a Component-Invariant Vernier Delay Line," Proc. IEEE International Test Conference, pp. 858-867, Baltimore, MD, 2001.			
	9	BHUNIA, S. et al., "Dynamic Supply Current Testing of Analog Circuits Using Wavelet Transform," Proc. IEEE VLSI Test Symposium, pp. 302-307, Monterey, CA, 2002.			

Examiner Signature		Date Considered	
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*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. ¹Applicant's unique citation designation number (optional). ²Applicant is to place a check mark here if English translation is attached.

This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, Washington, DC 20231. DO NOT SENT FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450. If you need assistance in completing the form, call 1-800-PTO-9199 (1-800-786-9199) and select option 2.